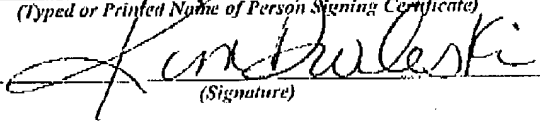


<b>CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)</b>			Docket No. <b>BUR920010100US1</b>
Applicant(s): <b>Joseph A. Iadanza</b>			
Application No. <b>09/683,872</b>	Filing Date <b>2/26/2002</b>	Examiner <b>Ortiz, Edgardo</b>	Group Art Unit <b>2815</b>
Invention: <b>METHOD OF CONNECTING CORE I/O PINS TO BACKSIDE CHIP I/O PADS</b>			
<b>RECEIVED</b> <b>CENTRAL FAX CENTER</b> <b>SEP 09 2004</b>			
<p>I hereby certify that this <u>Request for Reconsideration (15 pages)</u> (Identify type of correspondence)</p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>703-872-9306</u>)</p> <p>on <u>9/9/2004</u> (Date)</p> <p style="text-align: center;"><b>Kim Dwileski</b> (Typed or Printed Name of Person Signing Certificate)  (Signature)</p> <p style="text-align: center;">Note: Each paper must have its own certificate of mailing.</p>			

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**THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Joseph A. Iadanza

Group Art Unit: 2815

Filed: 2/26/2002

Examiner: Ortiz, Edgardo

Serial No.: 09/683,872

Docket No.: BUR920010100US1

Title: **METHOD OF CONNECTING CORE I/O PINS TO BACKSIDE CHIP I/O PADS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Request for Reconsideration**

Sir:

This Request for reconsideration is in response to the Final Office Action mailed July 14,  
2004.

09/683,872

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